AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings of claims in the Application.

Listing of Claims:

Claim 1 (Currently amended): A data buffer circuit for a video decoder comprising:

a receiver circuit ~~adapted~~ operable to receive a video bitstream;

a ring buffer ~~adapted~~ operable to receive the video bitstream, the ring buffer including a predetermined number of memory locations, the video bitstream being stored in either sequentially increasing or sequentially decreasing addresses as received from the receiver circuit, data stored in the ring buffer being accessible by both sequentially increasing memory addresses and sequentially decreasing memory addresses; and

an error resilience module ~~adapted~~ operable to select either an error correction procedure from a plurality of error correction procedures or to select no error correction procedure, the selection responsive to analysis of the video bitstream in both a forward direction and a reverse direction as accessed via ~~retrieve data from~~ the ring buffer.

Claim 2 (Currently amended): The data buffer circuit as ~~defined~~ recited in ~~Clam~~ Claim 1, wherein the receiver circuit comprises a wireless receiver.

Claim 3 (Currently amended): The data buffer circuit as ~~defined~~ recited in ~~Clam~~ Claim 1, further comprising a log interface circuit ~~adapted~~ operable to store data logging information in the ring buffer such that the data logging information is aligned with corresponding data from the video bitstream.

Claim 4 (Currently amended): The data buffer circuit as ~~defined~~ recited in ~~Clam~~ Claim 1, further comprising a VOP decoder ~~disposed~~ interposed in a data flow between the receiver circuit and the ring buffer such that the video bitstream stored ~~by~~ in the ring buffer is in a decoded form.

Claim 5 (Currently amended): A data buffer circuit for a video decoder comprising:

means for receiving a video bitstream;

means for inspecting the video bitstream for error and providing an error indication;

means for storing the video bitstream in a ring buffer subsequent to the video bitstream being inspected for error and prior to correction thereof, ~~regardless of an error indication~~ the video bitstream being stored in the ring buffer as video

bitstream data by the video bitstream storing means at either sequentially increasing addresses thereof or at sequentially decreasing addresses thereof, the video bitstream data being accessible by sequentially increasing memory addresses, sequentially decreasing memory addresses or by random access to a specified memory address;

means for storing data logging information corresponding to the video bitstream data in the ring buffer, ~~in an~~ the data logging information being aligned in memory ~~manner~~ with the corresponding video bitstream data; and

means for automatically retrieving both a portion of the video bitstream data from the ring buffer and a corresponding portion of the data logging information from the ring buffer ~~in response~~ responsive to a request ~~for data~~ for retrieving the portion of the video bitstream data from the ring buffer.

Claim 6 (Currently amended): A method of accessing information from a video bitstream comprising:

receiving a video bitstream;

inspecting the video bitstream for error;

storing the video bitstream in a ring buffer as video bitstream data, the storage occurring subsequent to the video bitstream inspecting step and prior to correcting any error encountered thereby ~~regardless of an error indication~~;

storing data logging information corresponding to the video bitstream data in the ring buffer, ~~in an~~ the data logging information being aligned in memory ~~manner~~ with the corresponding video bitstream data; and

automatically retrieving both a portion of the video bitstream data from the ring buffer and a corresponding portion of the data logging information from the ring buffer ~~in response~~ responsive to a request ~~for data~~ for retrieving the portion of the video bitstream data from the ring buffer.

Claim 7 (Currently amended): The method as ~~defined~~ recited in claim 6, further comprising the step of wirelessly receiving the video bitstream.

Claim 8 (Currently amended): The method as ~~defined~~ recited in claim 6, further comprising the step of receiving the video bitstream in an MPEG-4 compliant decoder.

Claim 9 (Currently amended): The method as ~~defined~~ recited in claim 6, further comprising the steps of:

decoding video object planes (VOPs) from the video bitstream prior to storing the video bitstream in the ring buffer; [[,]] and ~~wherein the~~

storing in the ring buffer the decoded VOPs as ~~of~~ the video bitstream data in the video bitstream storing step ~~comprises storing the decoded VOPs~~.

Claim 10 (Currently amended): The method as ~~defined~~ recited in claim 6, further comprising the steps of:

storing in the ring buffer video object planes (VOPs) as the video bitstream data in the video bitstream storing step;

retrieving the VOPs from the ring buffer upon demand therefor; and

decoding ~~video object planes (VOPs)~~ the VOPs subsequent to the retrieval thereof from the ring buffer ~~from the video bitstream after the video bitstream has been stored in the ring buffer~~.